

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
17	BRS	L17	68	(high adj2 compar\$6 or upper adj2 compar\$4 or high adj2 voltage or "vH" or "vtH" or High adj2 detect\$3 or upper adj2 detect\$2 or "vmax" or max\$6 near1 compar\$5) with (low adj2 compar\$6 or lower adj2 compar\$4 or low adj2 voltage or "vL" or "vtL" or low adj2 detect\$3 or upper adj2 detect\$2 or "vmin" or min\$5 near1 compar\$6) with (clock\$3 near2 logic or clock\$3 near3 flip adj2 flop\$1 or clock\$3 near2 "F/F" or clock\$3 near3 shift near2 register\$1 or clock\$3 near3 "S/H" or clock\$3 near2 sampl\$3 adj2 hold\$3)	US- PGPUB ; USPAT ; EPO; JPO	2005/12/31 10:33	

	Error Definition	Err ors
17		

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
18	BRS	L18	281	(high adj2 compar\$6 or upper adj2 compar\$4 or high adj2 voltage or "vH" or "vtH" or High adj2 detect\$3 or upper adj2 detect\$2 or "vmax" or max\$6 near1 compar\$5 or positive adj2 compar\$5 or positive adj2 detect\$3 or upper adj2 edg\$3 or ris\$3 adj2 edg\$3 or lead\$3 adj2 edg\$3) with (low adj2 compar\$6 or lower adj2 compar\$4 or low adj2 voltage or "vL" or "vtL" or low adj2 detect\$3 or upper adj2 detect\$2 or "vmin" or min\$5 near1 compar\$6 or negative adj2 detect\$3 or negative adj2 compar\$5) with (clock\$3 near2 logic or clock\$3 near3 flip adj2 flop\$1 or clock\$3 near2 "F/F" or clock\$3 near3 shift near2 register\$1 or clock\$3 near3 "S/H" or clock\$3 near2 sampl\$3 adj2 hold\$3 or clock\$3 near2 latch\$3 or lower adj2 edg\$3 or fall\$3 adj2 edg\$3 or lag\$4 adj2 edg\$3)	US- PGPUB ; USPAT ; EPO; JPO	2005/12/3 1 10:52	

	Error Definition	Err ors
18		

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
19	BRS	L19	682	(high adj2 compar\$6 or upper adj2 compar\$4 or high adj2 voltage or "vH" or "vtH" or High adj2 detect\$3 or upper adj2 detect\$2 or "vmax" or max\$6 near1 compar\$5 or positive adj2 compar\$5 or positive adj2 detect\$3 or upper adj2 edg\$3 or ris\$3 adj2 edg\$3 or lead\$3 adj2 edg\$3) with (low adj2 compar\$6 or lower adj2 compar\$4 or low adj2 voltage or "vL" or "vtL" or low adj2 detect\$3 or lower adj2 detect\$2 or "vmin" or min\$5 near1 compar\$6 or negative adj2 detect\$3 or negative adj2 compar\$5 or lower adj2 edg\$3 or fall\$3 adj2 edg\$3 or lag\$4 adj2 edg\$3) with (clock\$3 near2 logic or clock\$3 near3 flip adj2 flop\$1 or clock\$3 near2 "F/F" or clock\$3 near3 shift near2 register\$1 or clock\$3 near3 "S/H" or clock\$3 near2 sampl\$3 adj2 hold\$3 or clock\$3 near2 latch\$3)	US-PGPUB ; USPAT ; EPO; JPO	2005/12/31 10:54	
20	BRS	L20	83	19 and "375"/\$.ccls.	US-PGPUB ; USPAT ; EPO; JPO	2005/12/31 10:54	

21	BRS	L21	81	20 not 17	US- PGPUB ; USPAT ; EPO; JPO	2005/12/3 1 11:18	
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	Error Definition	Err ors
19		
20		
21		

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
22	BRS	L22	167	(differential adj2 amplif\$6 or differential adj3 driv\$3 or differential adj2 compar\$6 or differential adj2 buffer\$3 or differentail adj2 circuit\$3) with (clock\$3 near3 "F/F" or clock\$3 near3 "S/H" or clock\$3 near3 flip\$1 adj1 flop\$1 or clock\$3 near3 hold\$3 or clock\$3 near3 logic adj2 gate\$1 or clock\$3 near3 latch\$3 or clock\$3 near3 shif\$1 adj2 register\$1 or clock\$3 near3 "D" adj2 "F/F" or clock\$3 near3 "D" adj2 flip\$1 adj1 flop\$1 or clock\$3 near2 logic adj2 circuit\$3)	US- PGPUB ; USPAT ; EPO; JPO	2005/12/3 1 11:25	
23	BRS	L23	0	("6791370").URPN.	USPAT	2005/12/3 1 11:31	
24	BRS	L24	15	("4169233" "4608670" "5162681" "5243573" "5334890" "5534800" "5537066" "5552737" "5828239" "5838173" "5939903" "5949259" "6008673" "6037808" "6049504").PN.	US- PGPUB ; USPAT ; USOCR	2005/12/3 1 11:31	
25	BRS	L25	2	("6194933").URPN.	USPAT	2005/12/3 1 11:40	

	Error Definition	Err ors
22		
23		
24		
25		

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
26	BRS	L26	1	("5286972").PN.	US- PGPUB ; USPAT ; USOCR	2005/12/3 1 11:40	
27	BRS	L27	5	("6154548").URPN.	USPAT	2005/12/3 1 11:42	
28	BRS	L28	13	("6084607").URPN.	USPAT	2005/12/3 1 11:44	
29	BRS	L29	3	("4675696" "5404020" "5600350").PN.	US- PGPUB ; USPAT ; USOCR	2005/12/3 1 11:45	

	Error Definition	Err ors
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27		
28		
29		